

EAST: [10765810 IC power grids.wsp:1]

FileViewEditToolsWindowHelp

Failed

Saved

S2: (1) 09/848877

S3: (1736) "KONINKLUKE PHILIPS".as

S1: (0) 10/753814

S4: (0) 10/785810

S5: (5525124) conduct\$4 line

S8: (4387418) increas\$4

S7: (1689038) decreas\$4

S8: (2535388) thinn\$4 thick\$4 wider narrower

S9: (4798378) S6 S7

S10: (4264418) power supply

S11: (182438) S5 near8 S8

S12: (10427) S11 near8 S8

S13: (378) S10 with S12

S14: (2068003) (microchip micro adj chip chip integrated adj circuit die ic semicond

S15: (4) "6348721".pn. "8111310".pn.

S16: (10) Backward citation search 1

S17: (5) Forward citation search 1

S18: (8) Forward citation search 2

S19: (50) S13 same S14

S20: (18) S16 S18

Favorites

Tagged (5)

UDC

None

Q6s

US:PGPUB,USPAT,EPQ,JPO,DERWENT,IGM,TDB

Plurals

Highlight all hit terms initially

Default operator: OR

S18 S18

Dec. 2004

GRS form

ISAR form

Image

Text

HTML

	U	I	Inventor	Document	Issue	P	Title	Current	Current X8	Retrieval	S	C	P	Image	Doc	P
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	Buffet; Patri	US 6586828	2003	01	Integrated circuit bus grid having wires with	257/891	257/207		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6586828
2	<input type="checkbox"/>	<input type="checkbox"/>	Schultz; Rich	US 6111310	2000	09	Radially-increasing core power bus grid archi	257/681	257/668		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6111310
3	<input type="checkbox"/>	<input type="checkbox"/>	Arai; Kiyokaz	US 5145800	1992	01	Method of wiring for power supply to large-s	438/599	257/503		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5145800
4	<input type="checkbox"/>	<input type="checkbox"/>	Ohno; Kenichi	US 4255872	1981	01	Large scale semiconductor integrated circuit	328/47	257/205		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 4255872
5	<input type="checkbox"/>	<input type="checkbox"/>	Cano; Francis	US 6581201	2003	01	Method for power routing and distribution in	718/12	257/E23.15		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6581201
6	<input type="checkbox"/>	<input type="checkbox"/>	Cano; Francis	US 6308307	2001	11	Method for power routing and distribution in	718/9	257/E23.15		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6308307
7	<input type="checkbox"/>	<input type="checkbox"/>	Nakemori; Ts	US 5872740	1999	12	Layout algorithm for generating power suppl	438/128	257/E23.15		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5872740
8	<input type="checkbox"/>	<input type="checkbox"/>	Yao; Chingchi	US 5923089	1999	02	Efficient routing method and resulting struct	257/775	257/207		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5923089
9	<input type="checkbox"/>	<input type="checkbox"/>	Hamburgen;	US 5629840	1997	01	High powered die with bus bars	361/820	174/52.4		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5629840
10	<input type="checkbox"/>	<input type="checkbox"/>	Watanabe; M	US 5587610	1996	18	Semiconductor device having a conductive le	257/758	257/310		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5587610

HRs

Details

HTML

Ready

NTM